

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES  
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865 Filed: December 5, 2003

Examiner: Nguyen, Van Thu T. Group Art Unit: 2824

Atty. Docket No.: 023-0029 Confirmation No.: 8494

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June 12, 2006

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NON-FINAL OFFICE ACTION**

This paper is responsive to the Non-Final Office Action mailed on January 10, 2006, having a shortened statutory period for response set to expire April 10, 2006. Filed herewith is a Petition for a 2-month Extension of Time, thus extending the period for response until June 12, 2006 (June 10 falling on a Saturday). In light of the Amendments and/or Remarks herein, further examination is requested.

Any fees required by this paper are being provided as directed in an electronic submission of this paper or in a transmittal letter accompanying this paper. However, the Commissioner is hereby authorized to charge any deficiency in fees required by this paper and any additional fees under 37 C.F.R. § 1.16 or 1.17 which may be required during the pendency of this application, and to similarly credit any overpayment, to Deposit Account 50-0631.